

HP 1660-Series Logic Analyzers



- Get to the Root of Your Problems Quickly With These Portable Logic Analyzers
- Resolve Time Intervals as Short as 2 ns, and Find Elusive Glitches With 5 Timing Analysis Modes
- Get an Analog View of Critical Signals With an Integrated 2-Channel, 1 GSa/s Oscilloscope
- Display Disassembled Code Listings With Low-Profile, Low-Capacitance Interfaces For i960® Processors
- Integrate Code Flow, Timing Waveforms and Oscilloscope Waveforms Into a Single Display
- Focus on the Problem With Fast, Intuitive Operation Using the Graphic Human Interface
- Document Your Work Using Standard File Types and the Built-In 3.5" Floppy Disk

Label	ADDR	80960KB Inverse Assembler		M/R	S	
Base	Hex	Decimal (0x = Hex)		Symbol	S	
254	0B0049B0	C	<DATA READ>	0x00000000	READ	4
255	0B000A20	0	addo	16, sp, sp	READ	4
256	0B000A20	4	shla	4, 9, q0	READ	4
257	0B000A20	8	call	0x0B004830	READ	4
258	0B000A20	C	mov	q0, r3	READ	4
259	0B000A30	0	cmpabne	0, r3, 0x0B000A48	READ	4
260	0B000A30	4	call	0x0B004880	READ	4
261	0B000A30	8	sl	q0, 0x40(fp)	READ	4
262	0B000A30	C	ld	0x40(fp), r15	READ	4
263	0B004830	0	addo	16, sp, sp	READ	4
264	0B004830	4	lda	0x80055A0, q0	READ	4
265	0B004830	8	<DISPLACEMENT>	0x0B0055A0	READ	4
266	0B004830	C	ret		READ	4
267	0B01C6C0	0	<DATA WRITE>	0x0B01C680	WRITE	4
268	0B01C6C0	4	<DATA WRITE>	0x0B006800	WRITE	4
269	0B01C6C0	8	<DATA WRITE>	0x0B000040	WRITE	4

HP's 1660-series logic analyzers provide 100 MHz state and 500 MHz timing analysis in an affordable, portable logic analyzer. The i960 CA/CF CPU pre-processor interface can be used with either the HP 1660A (134 channels) or the HP 1661A (102 channels) logic analyzer. The 1660AS and 1661AS models have a built-in digitizing oscilloscope that samples at 1 GSa/sec on both channels, showing you what your timing signals really look like.

The HP E2432A Intel i960 CA/CF CPU preprocessor interface allows you to easily connect the logic analyzer to your i960 CA/CF processor-based target system. The preprocessor keeps track of the i960 CA/CF processor address pipeline and, when state traces are displayed, data is aligned with the correct address. An inverse assembler is included with the pre-processor that displays state listings in i960 microprocessor mnemonics.

PROCESSORS SUPPORTED:
i960 Sx/Kx/Jx/Cx/Hx, MC Processors

AVAILABILITY:
i960 KA/KB/MC preprocessors available from Corelis, Inc.
Phone: (310) 926-6727

CONTACT:
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For international contacts see Appendix B.

